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| NAME OF INVENTOR(S):<br>Barnett  | RECEIPT DATE & SERIAL NO.:<br><b>Serial No.: 10/696,827</b><br><b>Filing Date: 10/30/2003</b>  |
| TITLE OF INVENTION:<br>CIRCUIT AND METHOD TO COMPENSATE<br>FOR RMR VARIATIONS AND FOR SHUNT<br>RESISTANCE ACROSS RMR IN AN OPEN<br>LOOP CURRENT BIAS ARCHITECTURE  |  |
| TI FILE NO.:<br><b>TI-36061</b>  | DEPOSIT ACCT. NO.:<br><b>20-0668</b>   |
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